

## Product Summary

$V_{DS}$ (V)	$R_{DS(on),max}$ (mΩ)	$I_D$ (A)
30	10 @ $V_{GS} = 10V$	20 <sup>(1)</sup>

## Features

- ❖ Fast Switching
- ❖ Low On-Resistance
- ❖ Low Gate Charge

## Application

- ❖ Load Switch
- ❖ Motor Control
- ❖ Power Management

## General Information

### Shipping

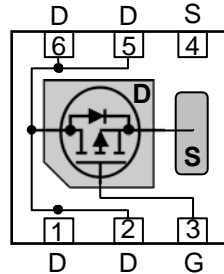
- ❖ One shipping options is offered as standard
- ❖ Un-sawn wafer

### Handling

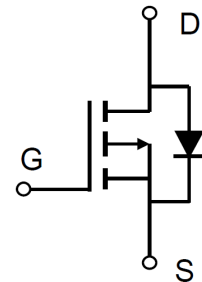
- ❖ Product must be handled only at ESD safe workstations. Standard ESD precautions and safe work environments are as defined in MIL-HDBK-263.
- ❖ Product must be handled only in a class 10,000 or better-designated clean room environmen



DFN2X2-6L



## Equivalent circuit



## Absolute maximum rating@25°C

Parameter		Symbol	Limit	Unit
Drain-source voltage		$V_{DS}$	30	V
Gate-source voltage		$V_{GS}$	±20	
Continuous drain current	$T_C=25^{\circ}C^{(1)}$	$I_D$	20	A
	$T_C=100^{\circ}C^{(1)}$		13	
Pulsed drain current <sup>(2)</sup>		$I_{D,pulse}$	80	
Avalanche energy, single pulse <sup>(3)</sup>		$E_{AS}$	16	mJ
Power dissipation	$T_C=25^{\circ}C$	$P_D$	6.2	W
Operating junction and storage temperature range		$T_J, T_{stg}$	-55 to 150	°C

## Thermal Characteristic

Parameter	Symbol	Max.	Unit
Thermal resistance, junction-to-case	$R_{\theta JC}$	20	°C/W

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	---	0.027	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	7.5	10	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	---	10	13	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1	1.5	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	-5.8	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
Q <sub>g</sub>	Total Gate Charge (4.5V)	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	19	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	6.4	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	5	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω I <sub>D</sub> =10A	---	7	---	ns
T <sub>r</sub>	Rise Time		---	6	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	26	---	
T <sub>f</sub>	Fall Time		---	7	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	---	950	---	pF
C <sub>oss</sub>	Output Capacitance		---	140	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	120	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	20	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,5</sup>		---	---	80	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =20A, dI/dt=100A/μs, T <sub>J</sub> =25°C	---	7	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge		---	6.5	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.5mH, R<sub>G</sub>=25R
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.



## Typical Performance Characteristics

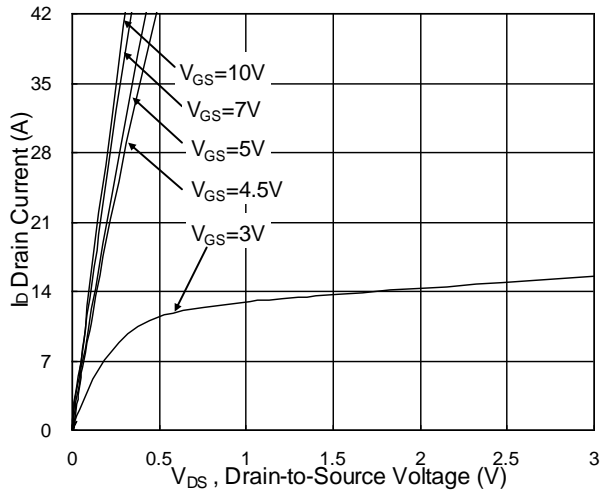


Fig.1 Typical Output Characteristics

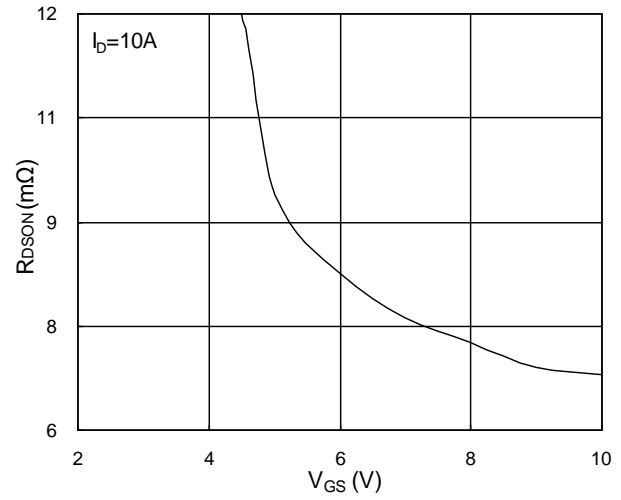


Fig.2 On-Resistance vs. Gate-Source

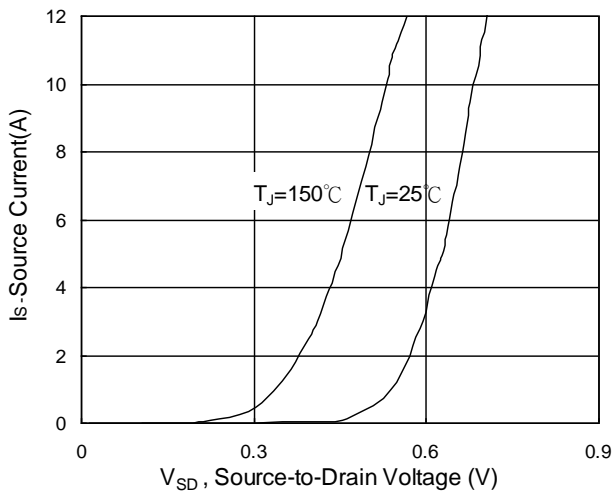


Fig.3 Forward Characteristics of reverse

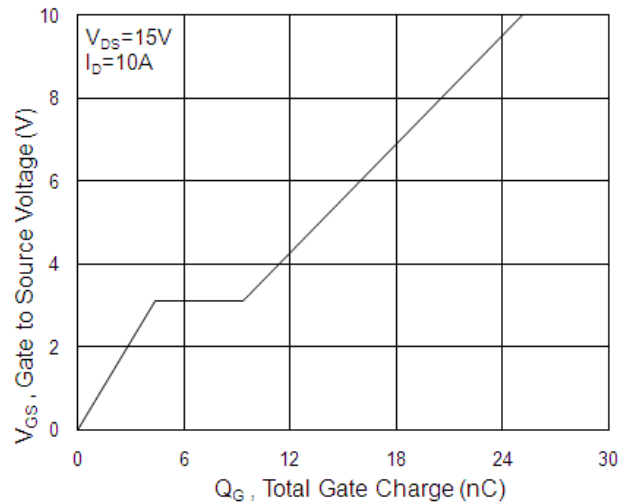


Fig.4 Gate-Charge Characteristics

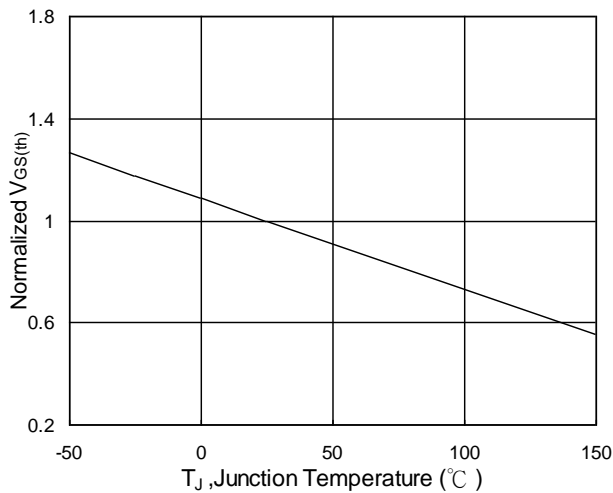


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

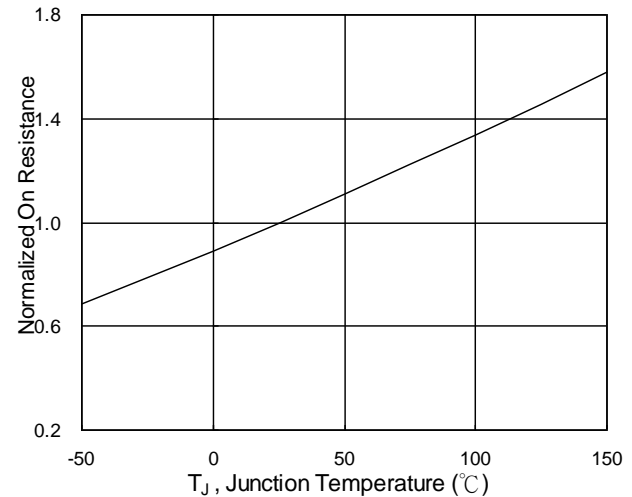


Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$



## Typical Performance Characteristics

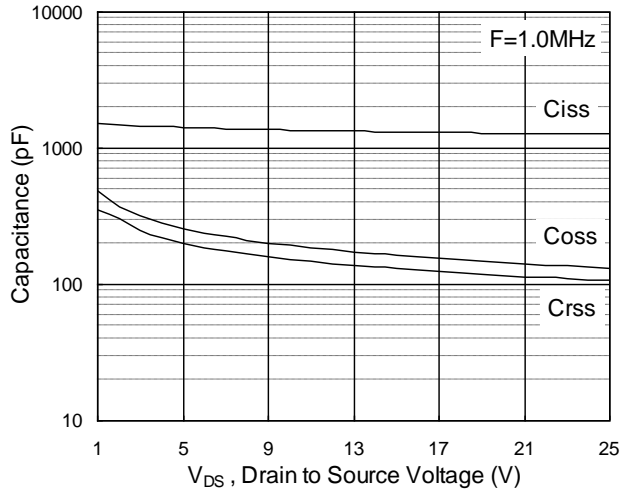


Fig.7 Capacitance

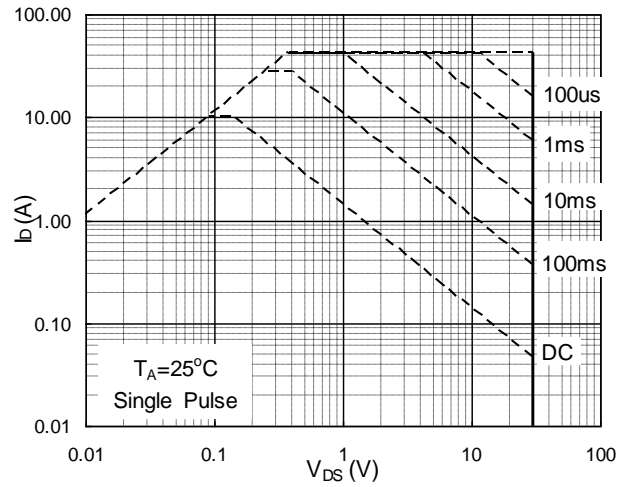


Fig.8 Safe Operating Area

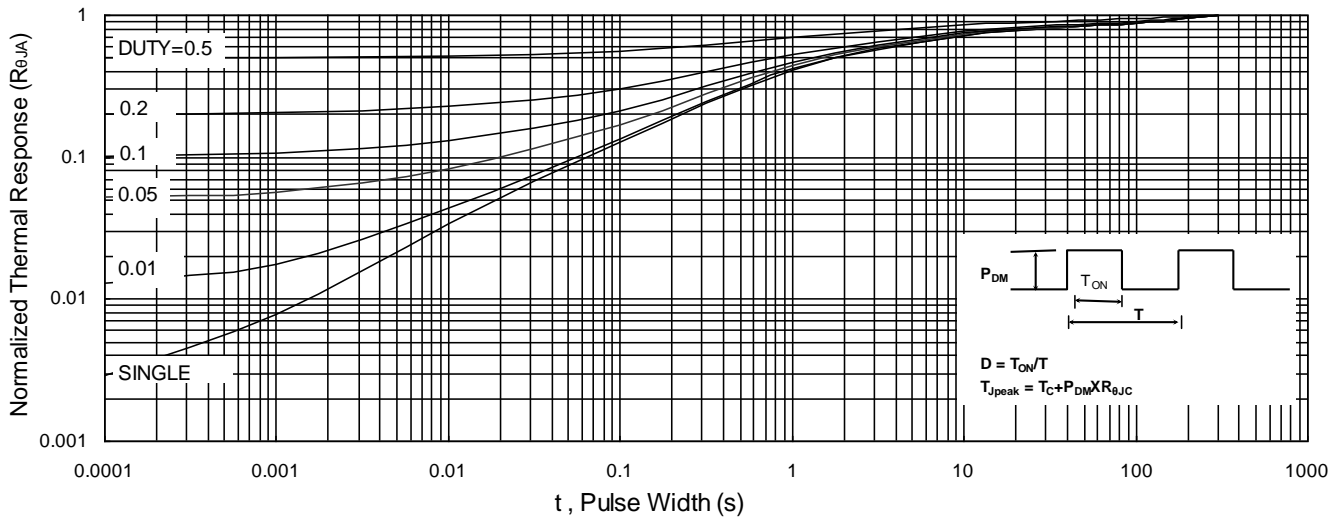


Fig.9 Normalized Maximum Transient Thermal Impedance

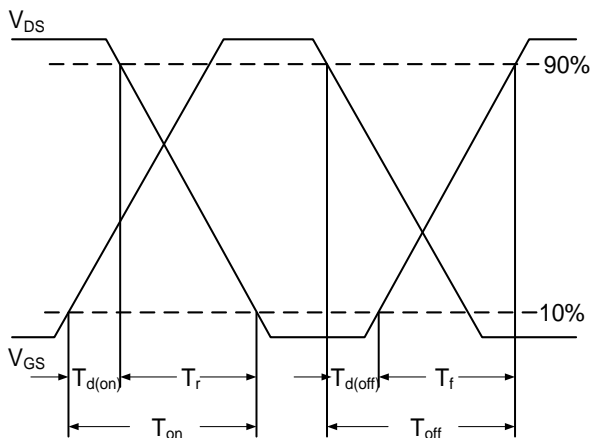


Fig.10 Switching Time Waveform

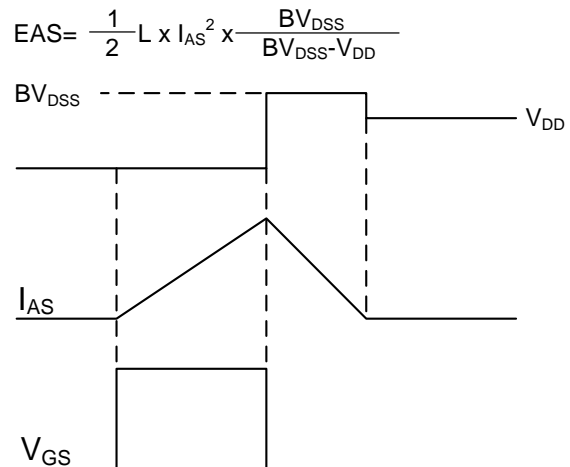
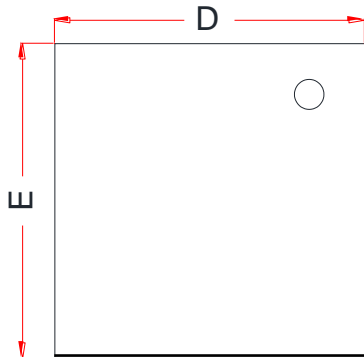
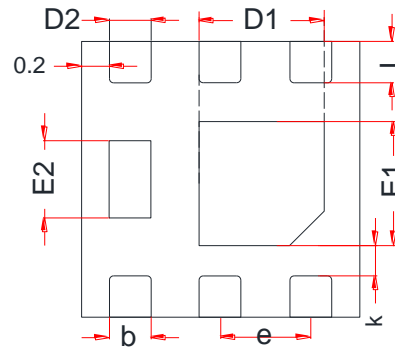
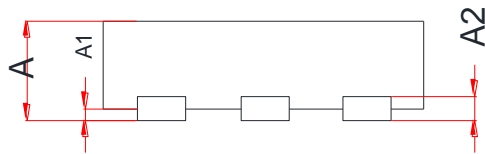


Fig.11 Unclamped Inductive Switching

**Outline Drawing DFN2X2-6L**

**TOP VIEW**

**BOTTOM VIEW**

**SIDE VIEW**

Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.70	0.75	0.85
A1	0.00	0.02	0.05
A2	0.20 Ref.		
b	0.25	0.30	0.35
D	1.95	2.00	2.05
D1	0.85	0.90	0.95
D2	0.25	0.30	0.35
E	1.95	2.00	2.05
E1	0.75	0.80	0.85
E2	0.56 Ref.		
e	0.65 BSC.		
L	0.30	0.35	0.40
K	0.20	-	-

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