

GOSTONE PROHS



GMS220N20K

N-Channel 200V,22mΩ max,SGT Power MOSFET

Product Summary					
V _{DS} (V)	$R_{DS(on),max}$ (m Ω)	I _D (A)			
200	22 @ V _{GS} = 10V	61 ⁽¹⁾			

Features

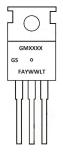
- Low R_{DS(on)} SGT technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Application

- DC/DC conversion
- Power switch
- Synchronous Rectification in SMPS



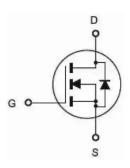
Equivalent circuit



TO-220

NOTE: LOGO - GS GMXXXXX- Part number code F - Fab location code A - Assembly location code Y - Year code

WW - Week code L&T - Assembly lot code



Absolute maximum rating@25℃

Parameter		Symbol	Limit	Unit
Drain-source voltage			200	V
Gate-source voltage			±20	
	T _C =25°C ⁽¹⁾		61	А
Continuous drain current	T _C =100°C	l _D	43	
Pulsed drain current ⁽²⁾			244	
Avalanche energy, single pulse ⁽³⁾		E _{AS}	542	mJ
Dower dissination	Tc=25°C	D	254	W
Power dissipation	T _A =25°C ⁽⁴⁾	P _D	127	
Operating junction and storage temperature range			-55 to 150	°C

Thermal Characteristic

Parameter	Symbol	Max.	Unit	
Thermal resistance, junction-to-case	Steady state	R _{eJC}	0.59	°C/W
Thermal resistance, junction-to-ambient (4)	Steady state			C/VV



Electrical Characteristics (TJ=25 °C unless otherwise noted)

Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Unit	
Static parameter							
Drain to source breakdown voltage	$V_{(BR)DSS}$	V _{GS} = 0, I _D = 250 μA				V	
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	2.5	3.4	4.5	V	
Gate-body leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			1	μA	
Drain-source on-resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		20	22	mΩ	
Forward transconductance (5)	g _{fs}	V _{DS} = 5 V, I _D = 20 A		40		S	
Gate resistance	Rg	f = 1 MHz		2.1		Ω	
Dynamic (5)							
Total gate charge V _{GS} = 10 V	Q_g			35		nC	
Gate-source charge	Q_{gs}	V _{DS} = 100 V, I _D = 20 A, V _{GS} = 10 V		11.5			
Gate-drain charge	Q_{gd}			7.5			
Turn-on delay time	t _{d(on)}			8.6			
Rise time	tr	V _{DS} = 100 V, I _D = 20 A, V _{GS} = 10 V,		17		ns	
Turn-off delay time	t _{d(off)}	$R_{GEN} = 3 \Omega$		28			
Fall time	t _f			22		7	
Input capacitance	C _{iss}			2363			
Output capacitance	C _{oss}	V _{DS} = 100 V, V _{GS} = 0 V, f = 1 MHz		184		pF	
Reverse transfer capacitance	C _{rss}			12.1			
Reverse Diode Characteristics (5)							
Diode forward voltage	V _{SD}	V _{GS} = 0 V, I _F = 2 A		0.7	1.2	V	
Reverse recovery time	t _{rr}	1 - 20 A dildt - 400 A/v-		100		ns	
Reverse recovery charge	Qrr	I _F = 20 A, di/dt = 100 A/μs		419		nC	

Notes

- (1) Package limited.
- (2) Pulse width limited by maximum junction temperature.
- (3) $V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, L = 1.0 \text{ mH}.$
- (4) Device mounted on FR-4 substrate PC board with 2oz copper in 1inch square cooling area.
- (5) Guaranteed by design, not subject to production testing.



Typical Performance Characteristics

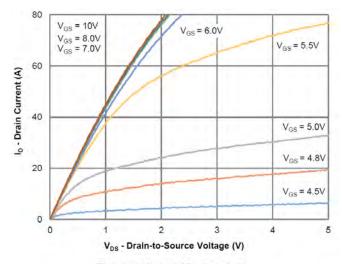


Figure 1: Output Characteristics

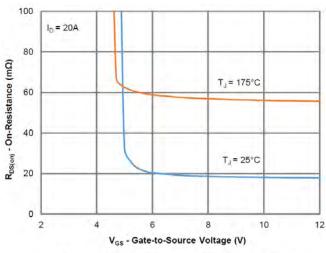


Figure 3: On-Resistance vs. Gate-Source Voltage

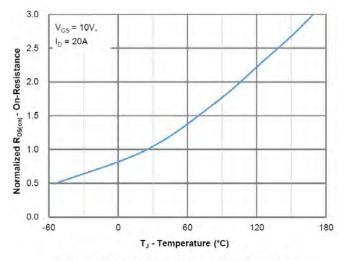


Figure 5: On-Resistance vs. Junction Temperature

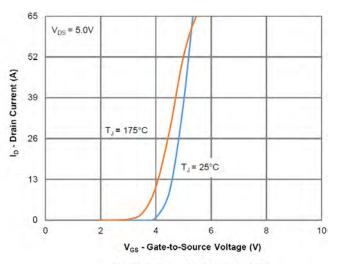


Figure 2: Transfer Characteristics

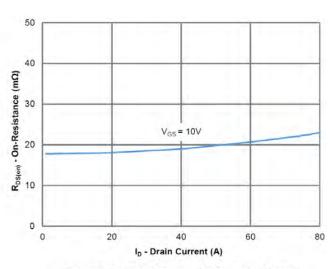


Figure 4: On-Resistance vs. Gate-Source Voltage

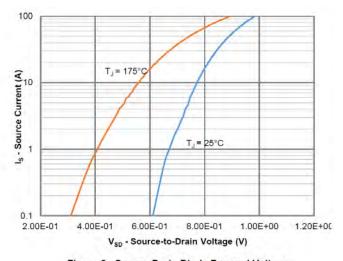


Figure 6: Source-Drain Diode Forward Voltage



Typical Performance Characteristics

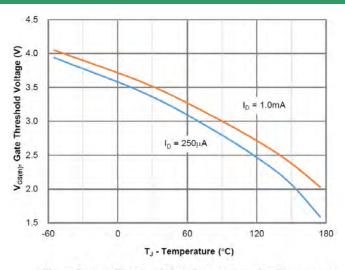


Figure 7: Gate Threshold Variation vs. Junction Temperature

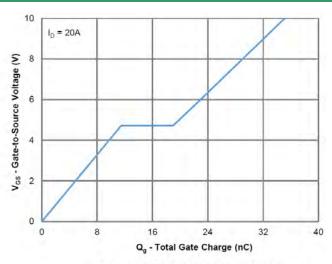


Figure 8: Gate Charge Characteristics

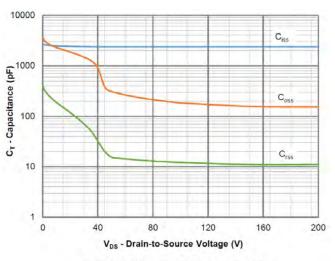


Figure 9: Capacitance Characteristics

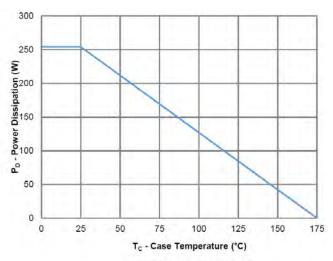


Figure 10: Power Derating

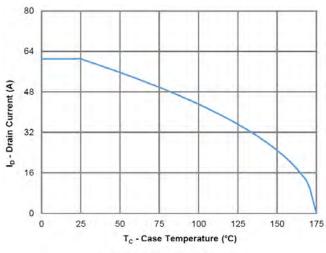


Figure 11: Current Derating

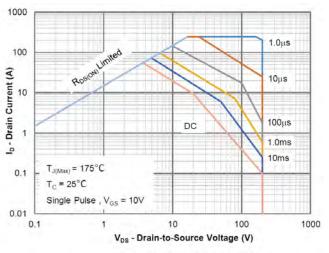


Figure 12: Safe Operating Area



Typical Performance Characteristics

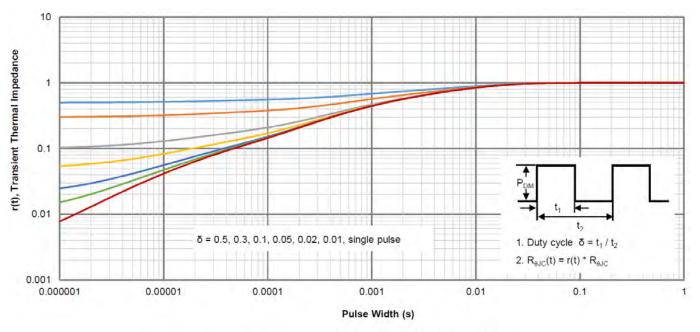
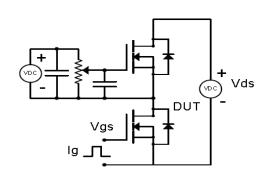


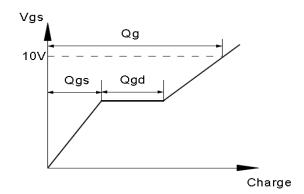
Figure 13: Normalized Maximum Transient Thermal Impedance



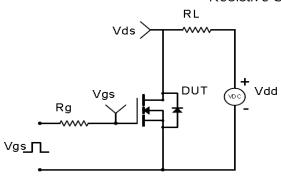
Test Circuit & Waveform

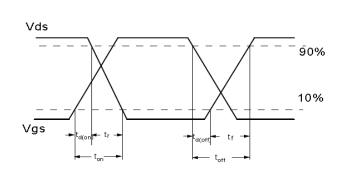
Gate Charge Test Circuit & Waveform



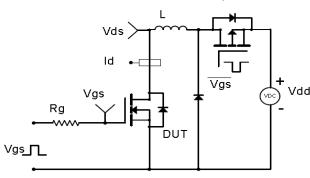


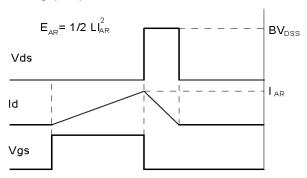
Resistive Switching Test Circuit & Waveforms



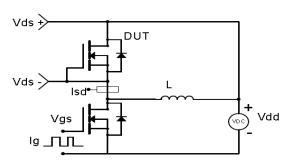


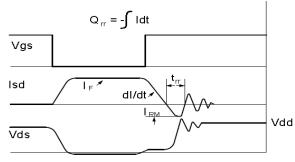
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms





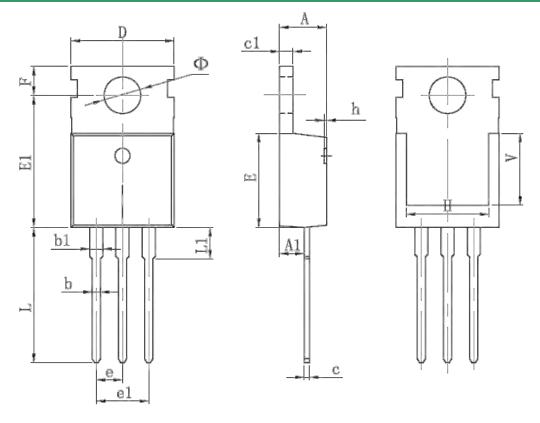
Diode Recovery Test Circuit & Waveforms







Outline Drawing TO-220



Symbol	Dimensions In Millimeters				
	Min.	NOM.	Max.		
Α	4.40	-	4.60		
A1	2.25	-	2.55		
b	0.71	-	0.91		
b1	1.17	-	1.37		
С	0.33	-	0.65		
c1	1.20	-	1.40		
D	9.91	-	10.25		
E	8.95	-	9.75		
E1	12.65	-	13.05		
е	2.54 TYP-				
e1	4.98	-	5.18		
F	2.65	-	2.95		
Н	7.90	-	8.10		
h	0.00	-	0.30		
L	12.90	-	13.40		
L1	2.85	-	3.25		
V	6.90 REF				
ф	3.40	-	3.80		



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